

U.S. UTILITY Patent Application

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| SCANNED <i>GR</i> | O.I.P.E. <i>GR</i> | PATENT DATE |
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| APPLICATION NO. 09/932053 | CONT/PRIOR F | CLASS 257 438 | SUBCLASS 455 | ART UNIT 2815 2823 | EXAMINER H. Lee |
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APPLICANTS

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Semiconductor package and fabricating methods

3. Results

PTO-2040
12/89[illegible]

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|------------------------------|-------------------|-------------------|-----------------------------------|-----------------------------|
| DRAWINGS | | | CLAIMS ALLOWED | |
| Sched. Drwg. | Pos. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| | | | NOTICE OF ALLOWANCE MAILED | |
| (Applicant Examiner) | | | | |
| (Date) | | | | |
| | | | ISSUE FEE | |
| | | | Amount Due | Date Paid |
| (Patent Examiner) | | | | |
| (Date) | | | | |
| | | | ISSUE BATCH NUMBER | |
| (Legal Instruments Examiner) | | | | |
| (Date) | | | | |

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